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42390.P9301

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Manoj Khare et al.

Examiner: Tran, Denise

Serial No.: 09/643,380

Art Unit: 2185

Filing Date: August 21, 2000

For: A METHOD AND APPARATUS
FOR PIPELINING ORDERED
INPUT/OUTPUT TRANS-
ACTIONS TO COHERENT
MEMORY IN A DISTRIBUTED
MEMORY, CACHE COHERENT
MULTI-PROCESSOR SYSTEM

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Dear Sir:

In response to the Final Office Action mailed May 18, 2005 and prior to the examination of the Request for Continued Examination filed concurrently herewith, please enter the following amendments and consider the following remarks.

Amendments to the Claims are reflected in the listing of claims, which begins on page 2 of this paper.

Remarks/Arguments begin on page 9 of this paper.

CERTIFICATE OF TRANSMISSION

I hereby certify that this correspondence is being facsimile transmitted to the U.S. Patent and Trademark Office on the date shown below.

By: Anne Collette
Anne Collette

Date: October 18, 2005